

FIG. 1

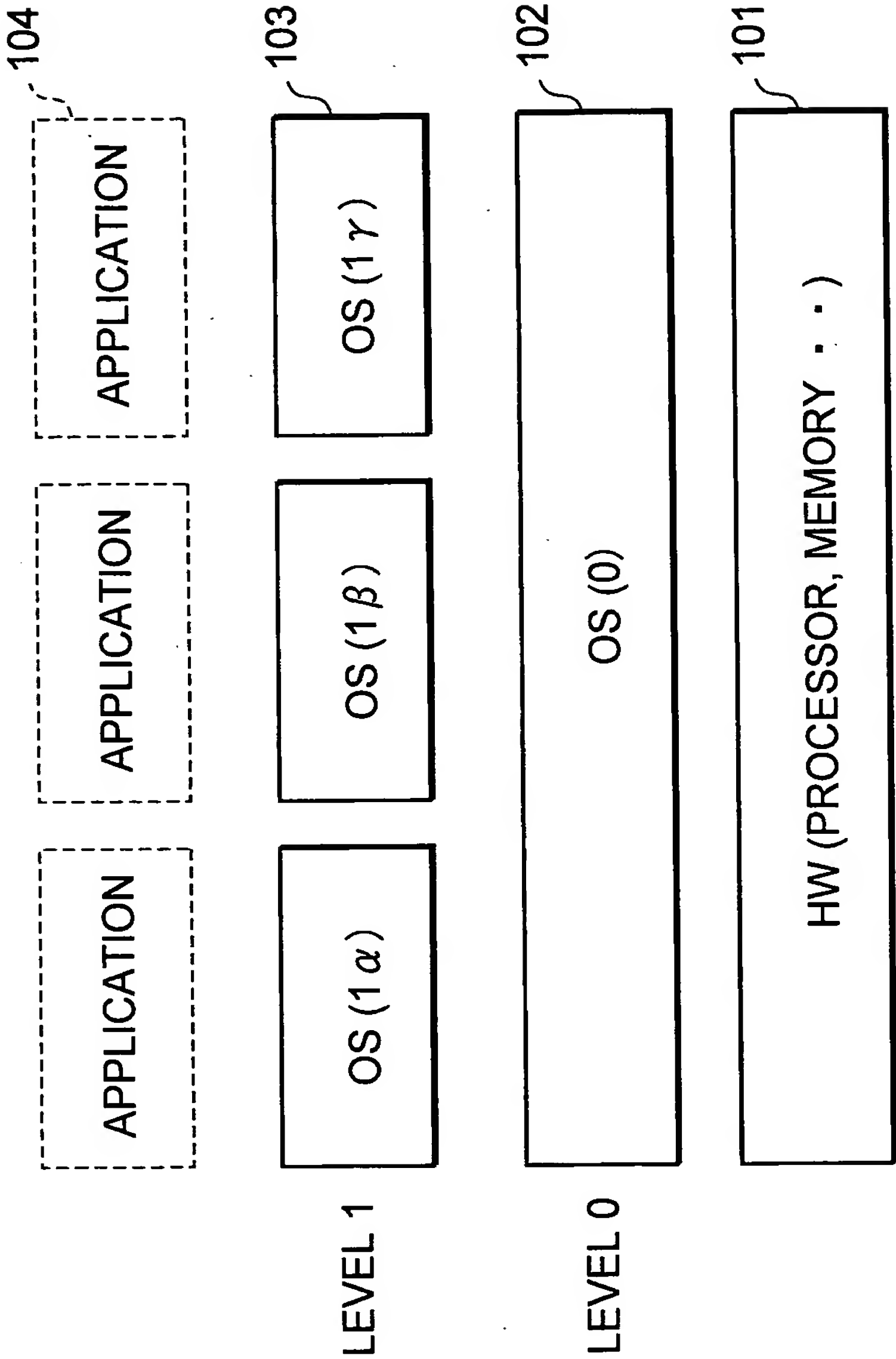
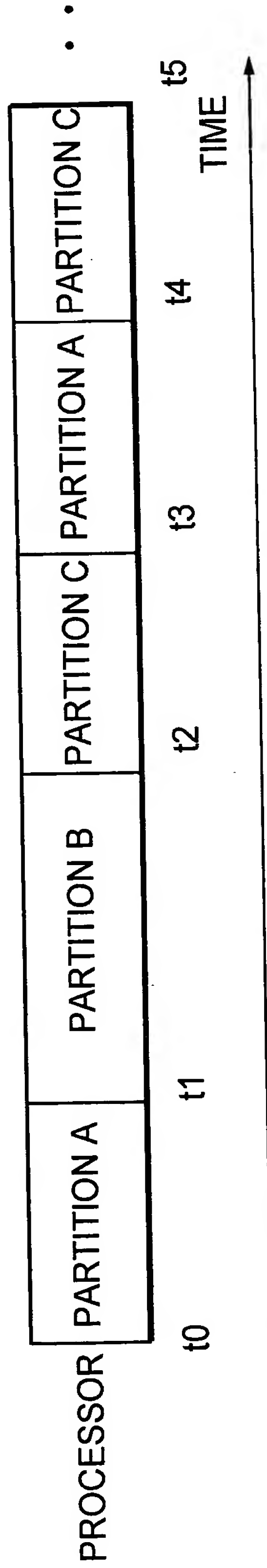


FIG. 2

(a) EXAMPLE PARTITION SCHEDULING IN SINGLE-PROCESSOR SYSTEM



(b) EXAMPLE PARTITION SCHEDULING IN MULTI-PROCESSOR SYSTEM

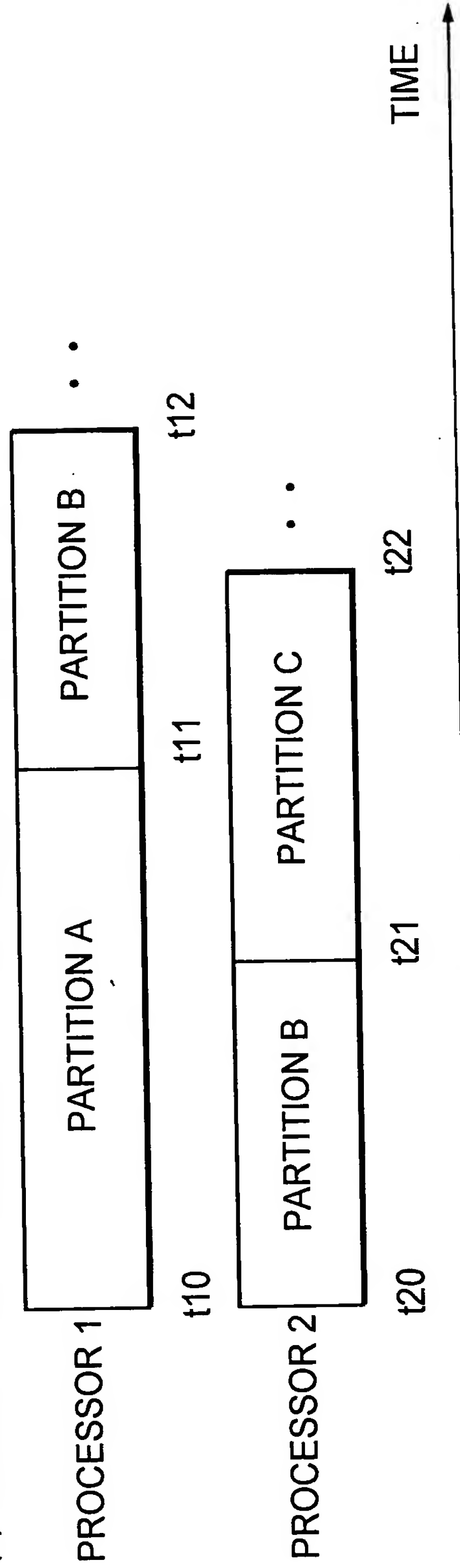


FIG. 3

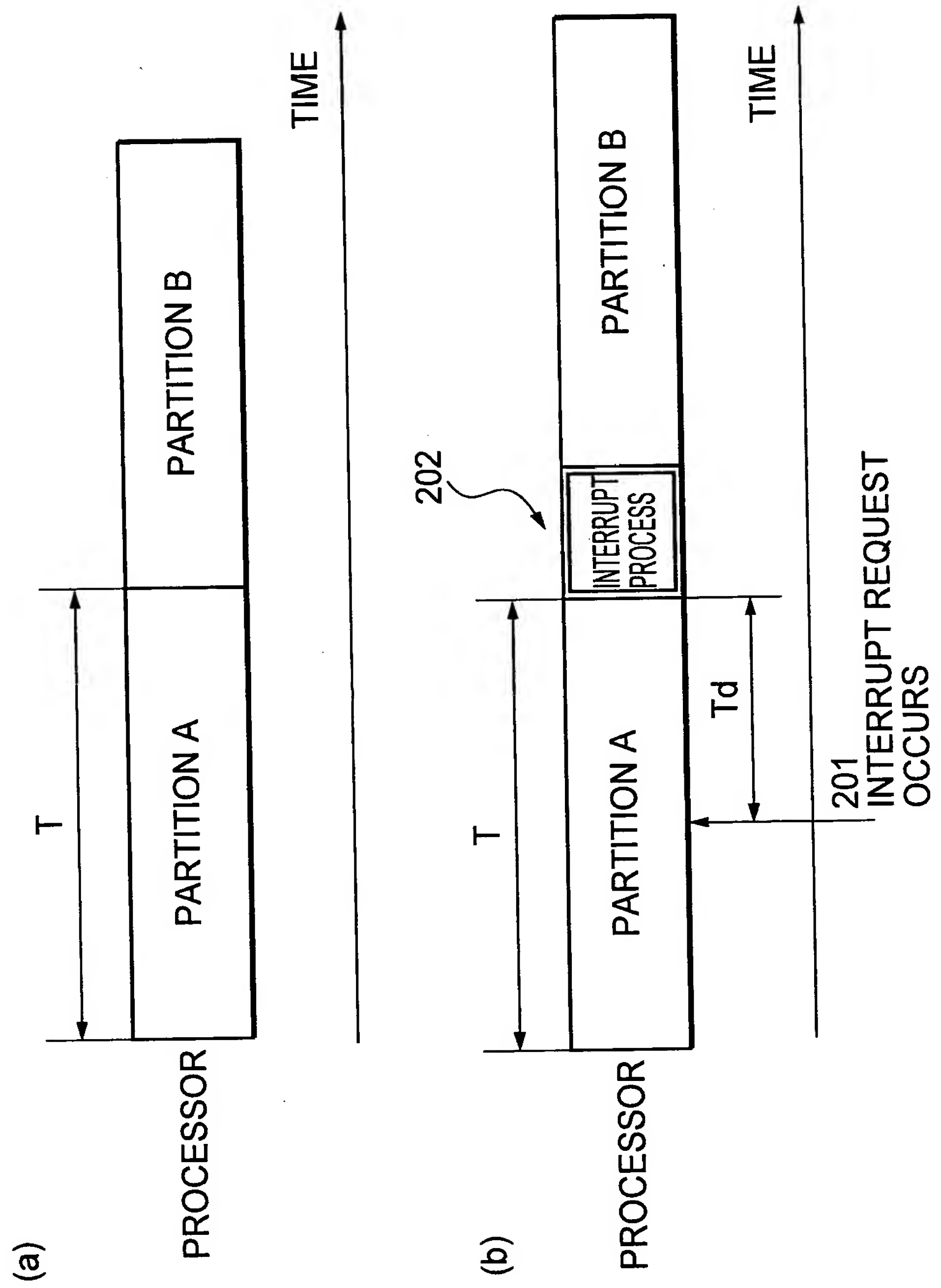
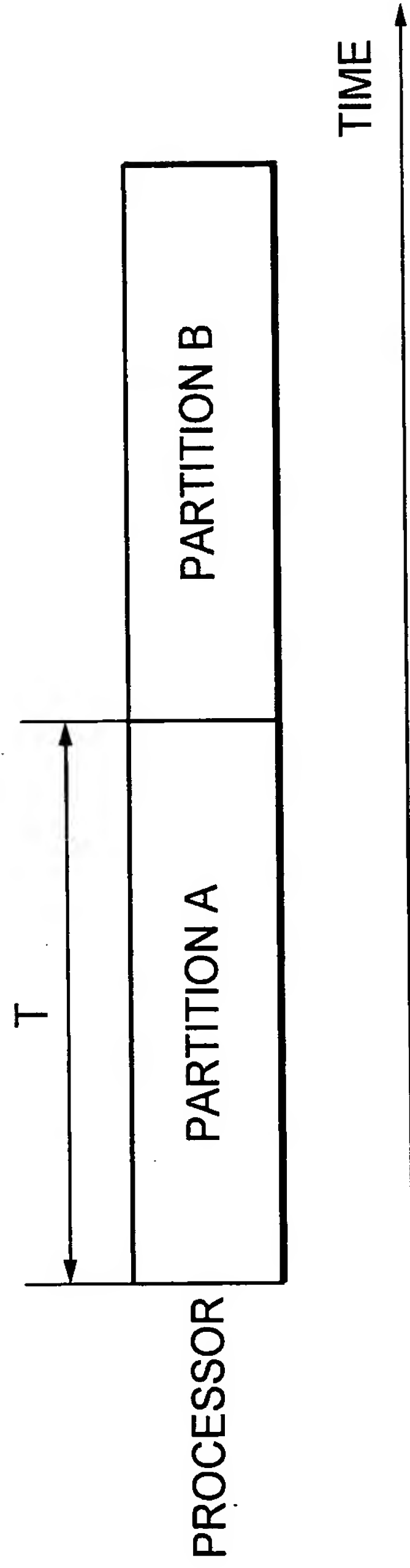
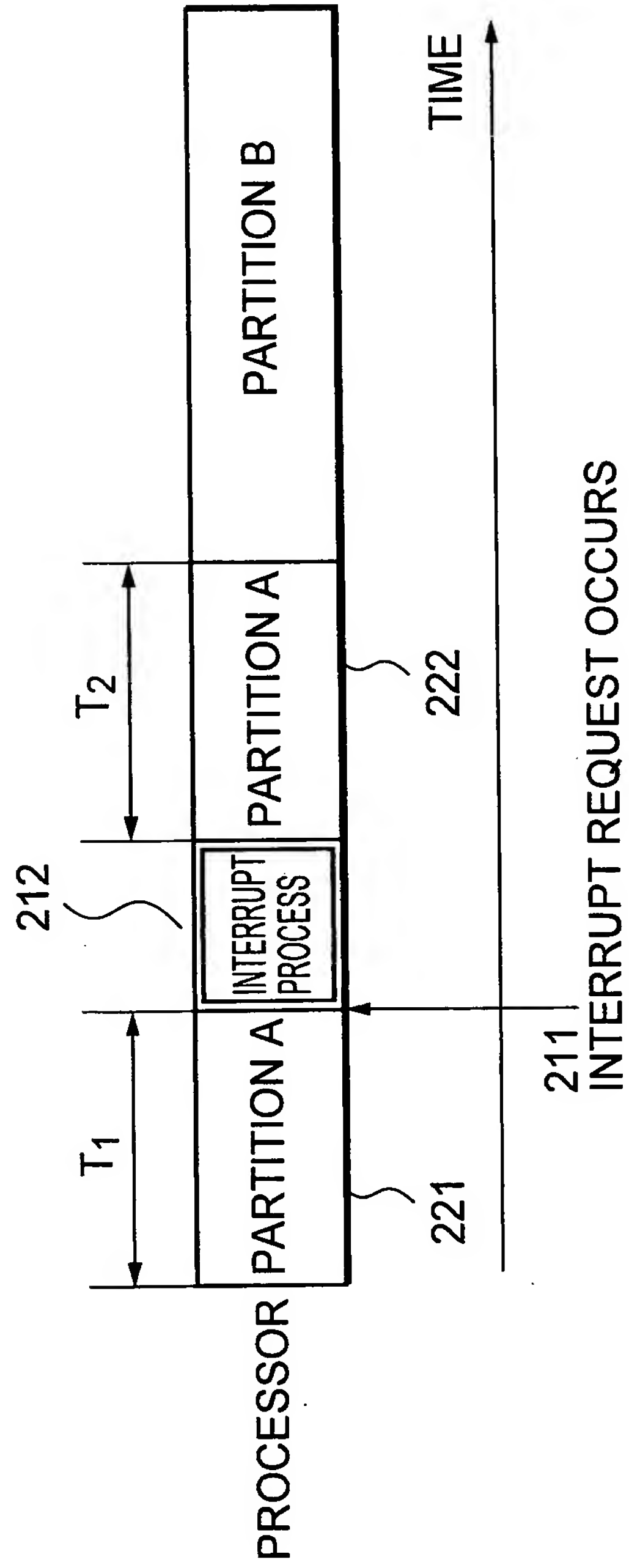


FIG. 4



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FIG. 5

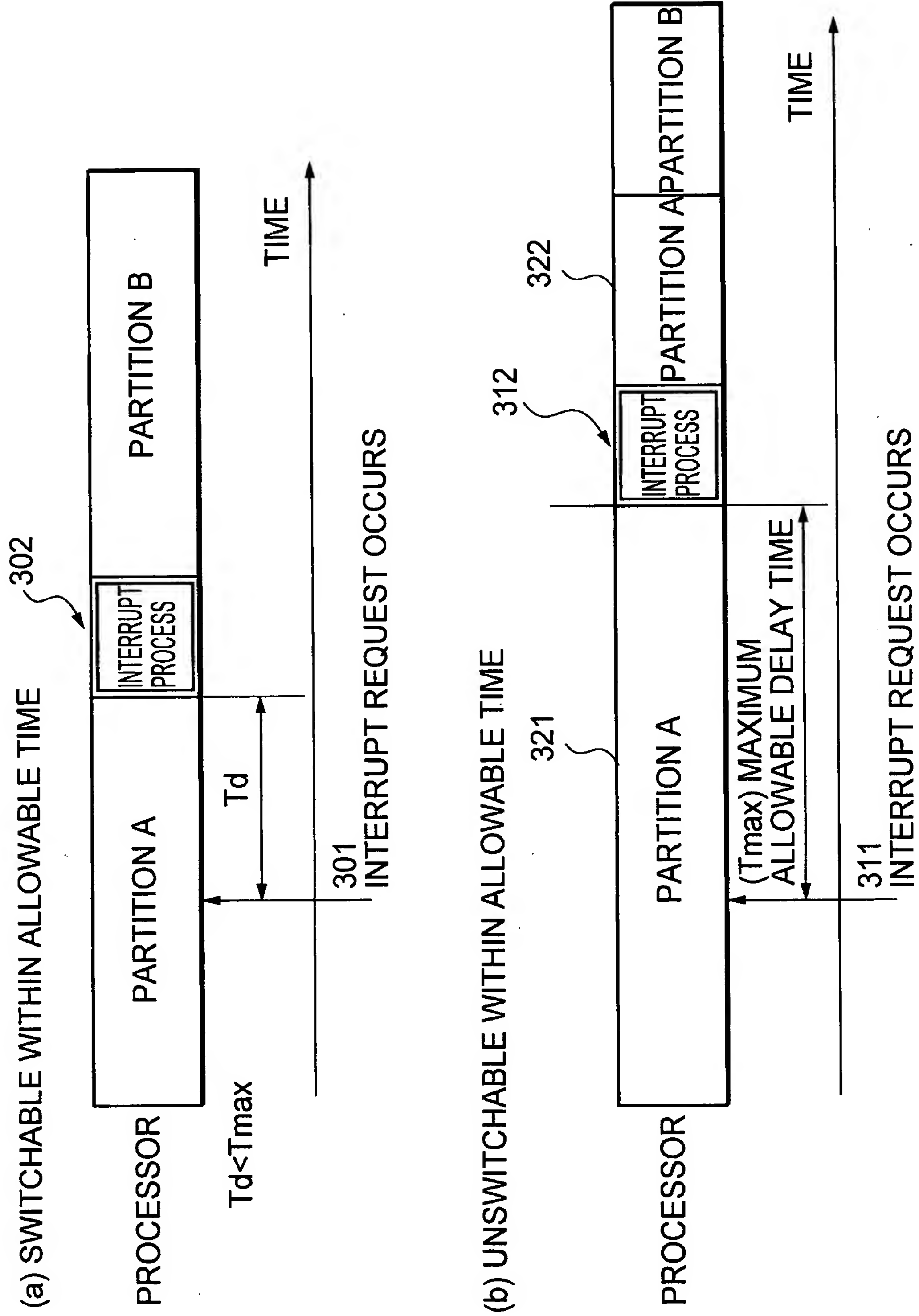


FIG. 6

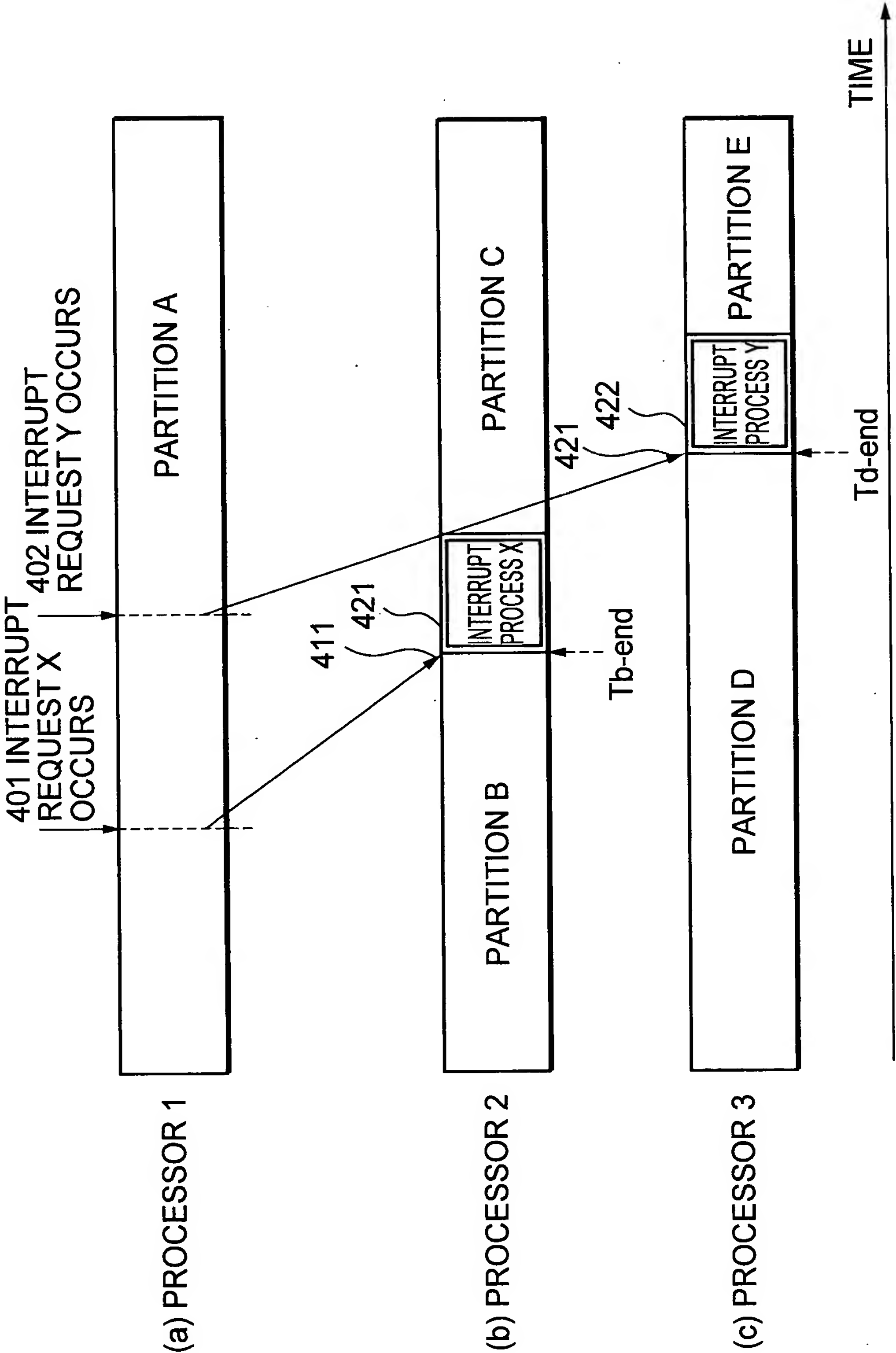


FIG. 7

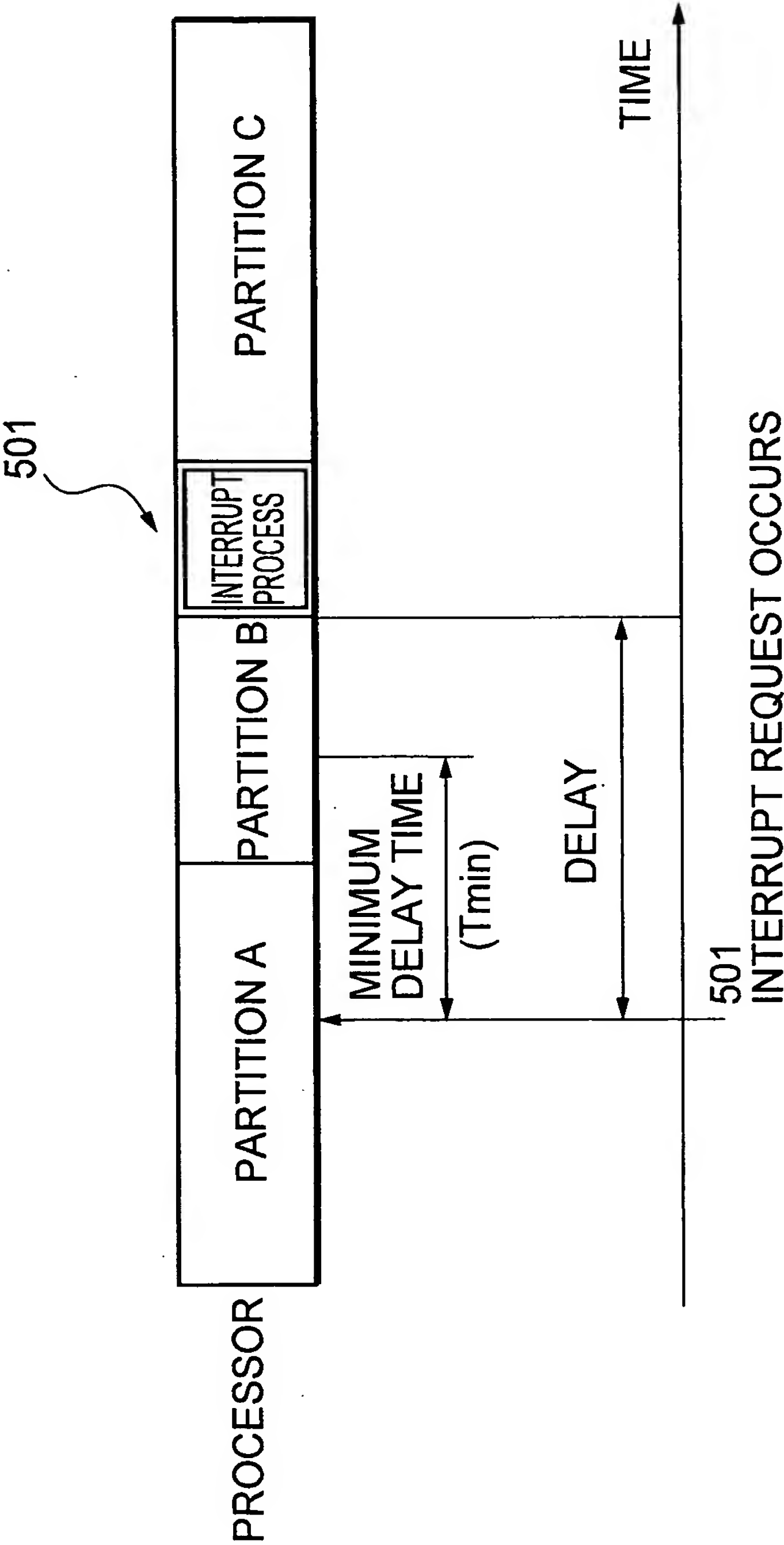


FIG. 8

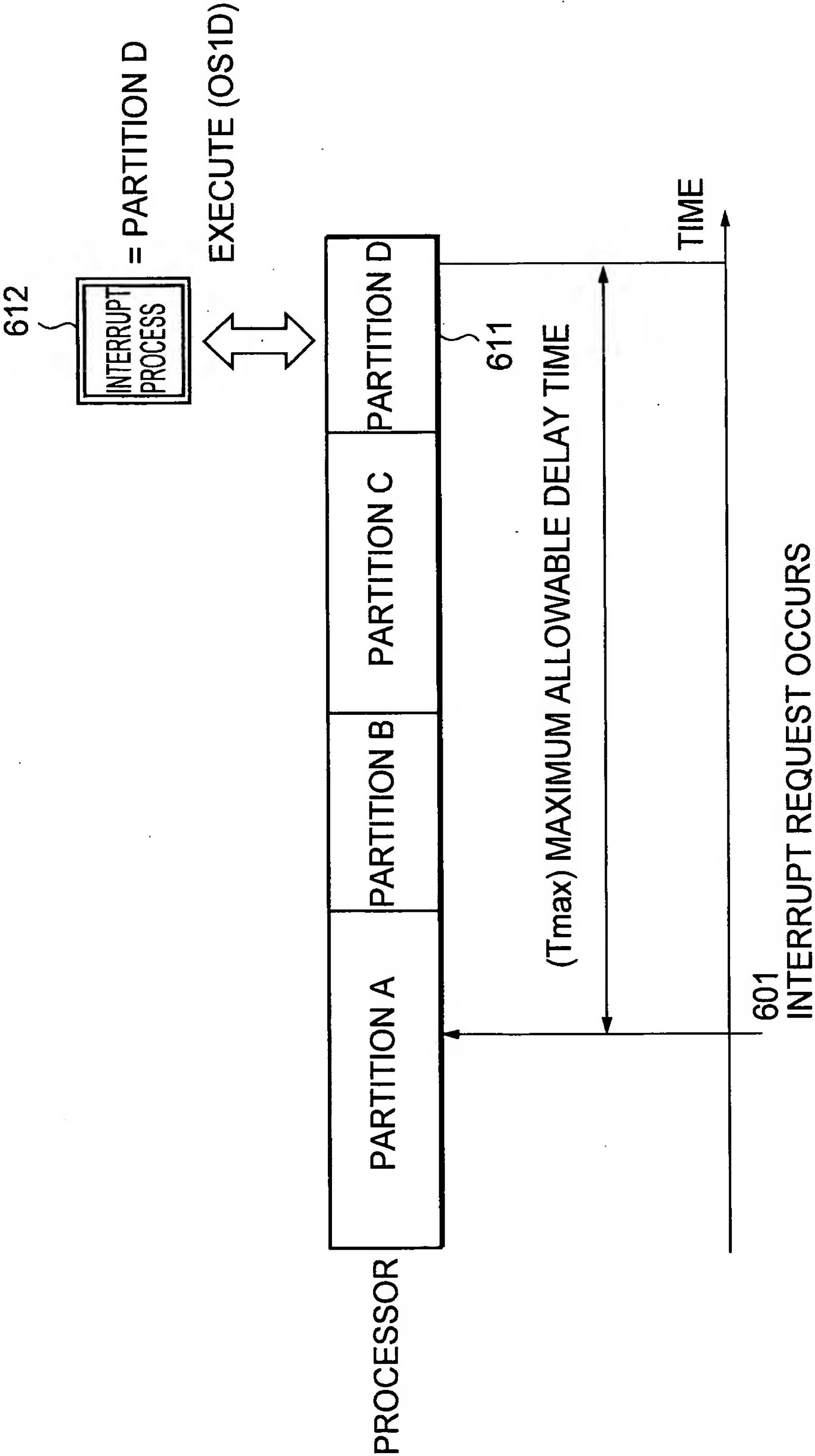




FIG. 9

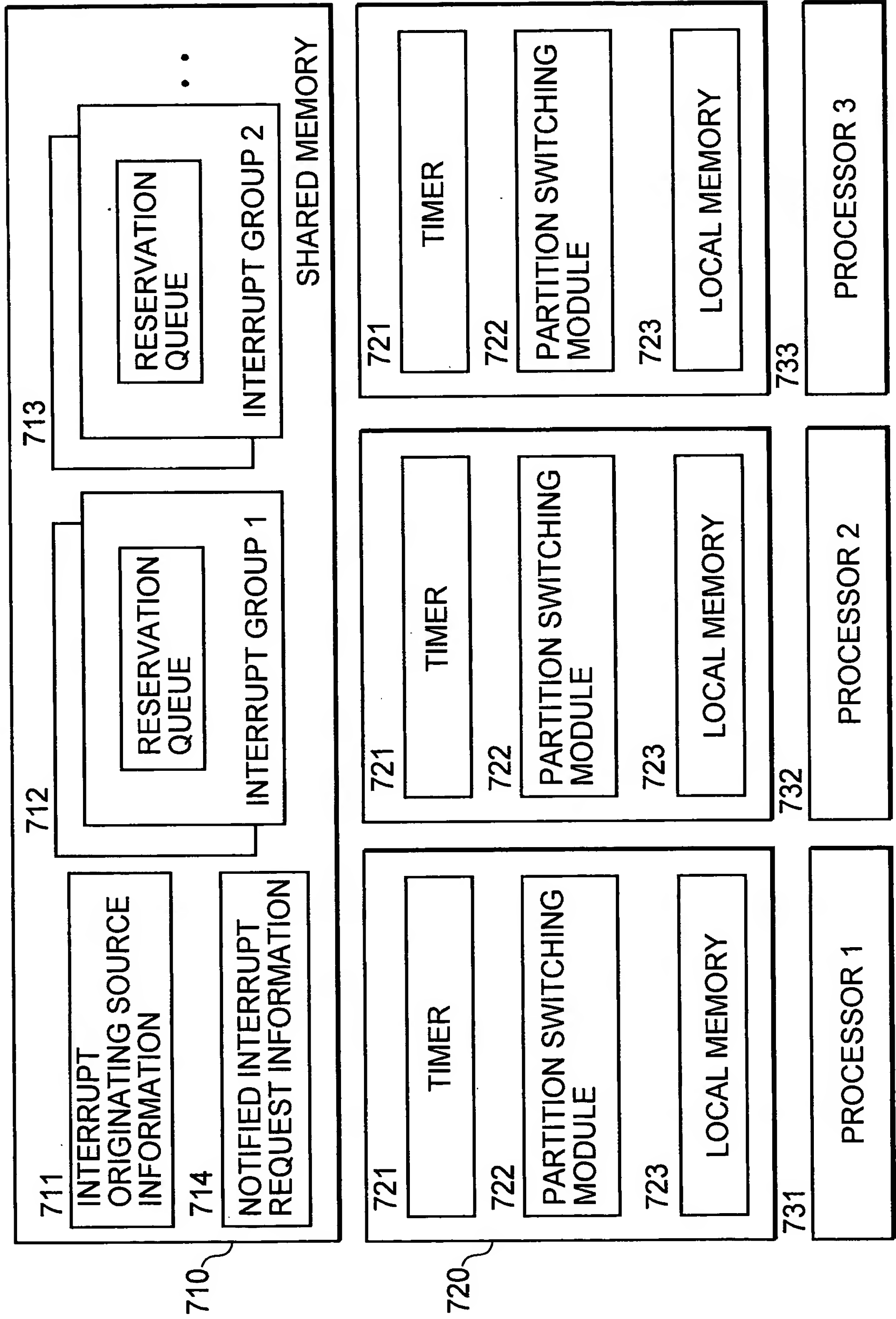


FIG. 10

INTERRUPT ORIGINATING SOURCE	INTERRUPT GROUP NUMBER	INTERRUPT PROCESSING PARTITION ID	MAXIMUM ALLOWABLE DELAY TIME	MINIMUM ALLOWABLE DELAY TIME
NETWORK INTERFACE	1	00031abc77	00:00:02.44'21"	00:00:00.12'21"
:	:	:	:	:

FIG. 11

INTERRUPT GROUP NUMBER	INTERRUPT ORIGINATING SOURCES
1	INTERRUPT ORIGINATING SOURCES a,b,c
2	INTERRUPT ORIGINATING SOURCES d,e,f
3	INTERRUPT ORIGINATING SOURCES g,h,i
:	:

FIG. 12

INTERRUPT REQUEST ID	MAXIMUM ALLOWABLE DELAY TIME	MINIMUM ALLOWABLE DELAY TIME
023ab...d75	00:00:02.44'21"	00:00:00.12'21"
:	:	:
131cd...e32	00:01:21.23'22"	00:00:00.23'31"

FIG. 13

CURRENT PARTITION ID	INTERRUPT GROUP	PARTITION CONTEXT
00021abc77	1, 3	REGISTER SETTING VALUES NOTIFIED INTERRUPT SET PREDICTED START TIME
001232bc21	2	
:	:	
01432ead11	1	

FIG. 14

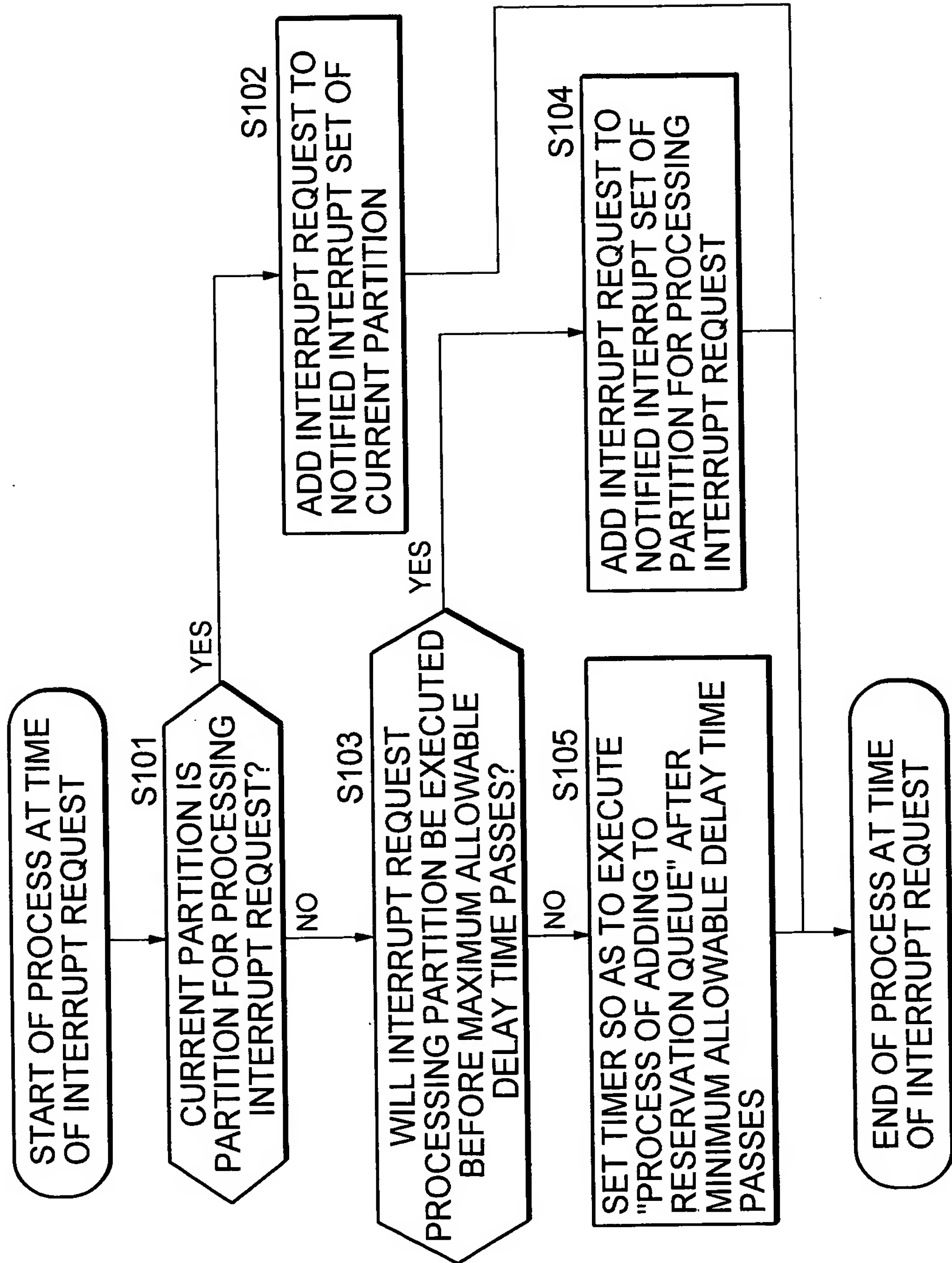
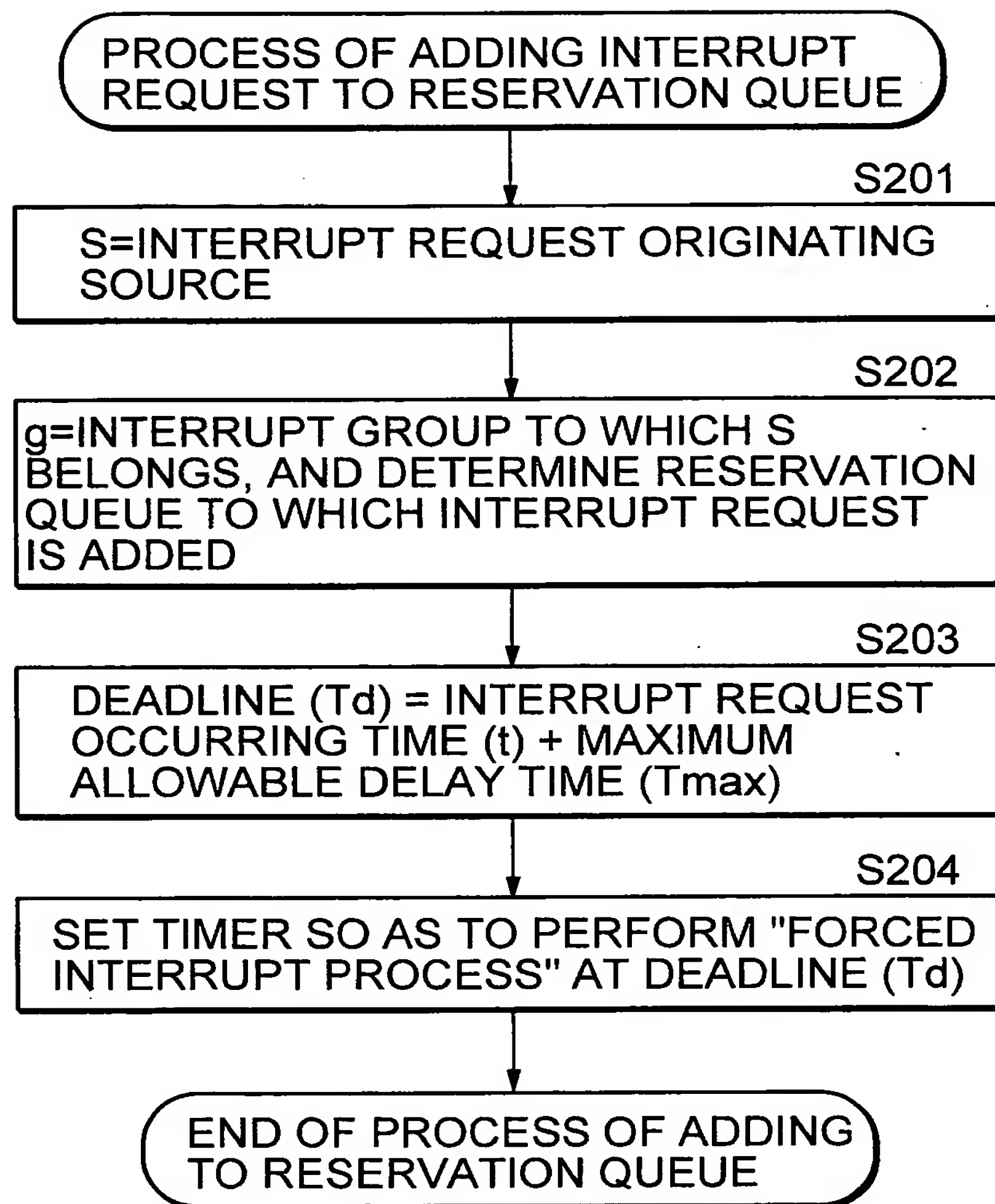
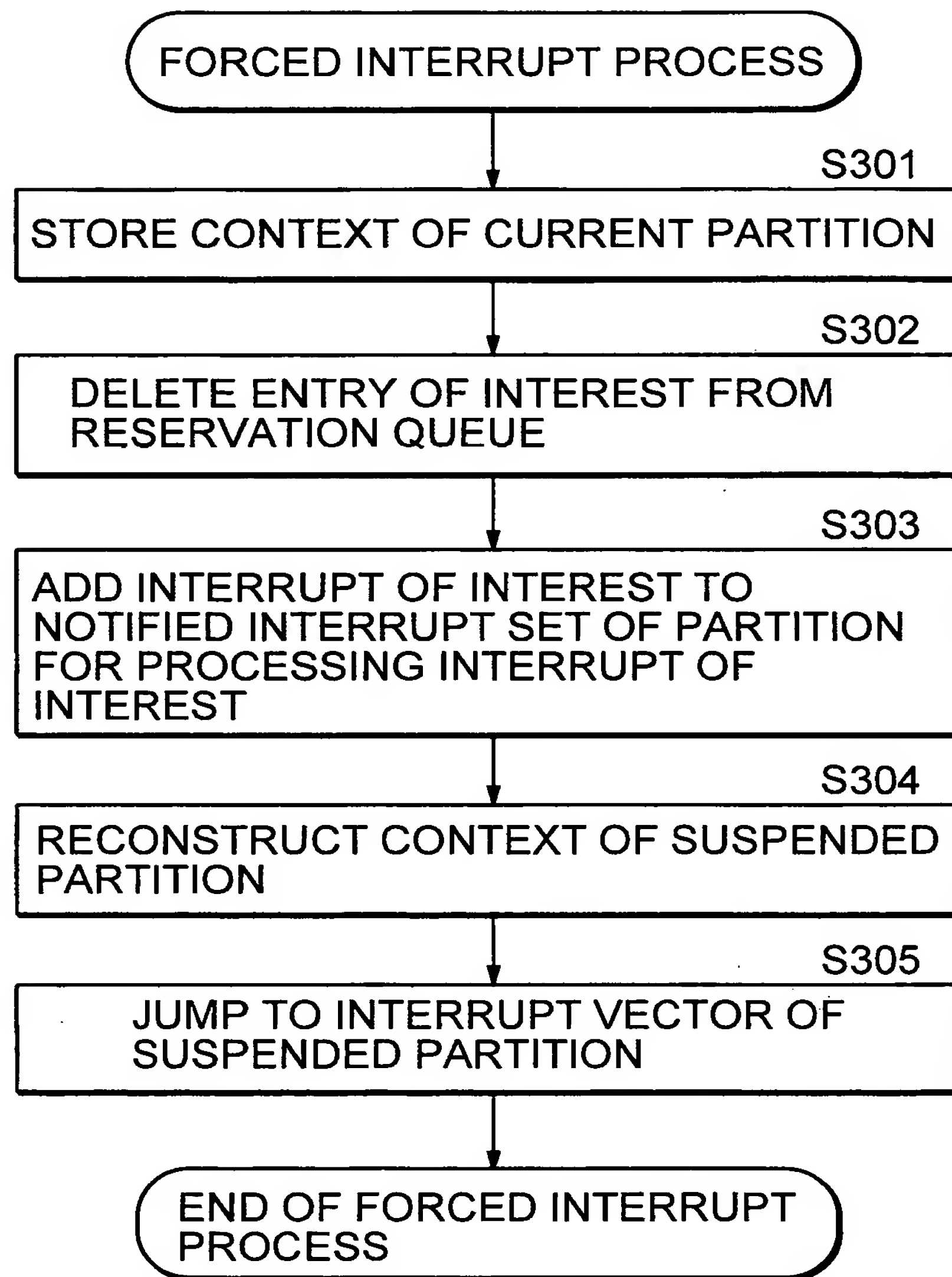


FIG. 15



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FIG. 16



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FIG. 17

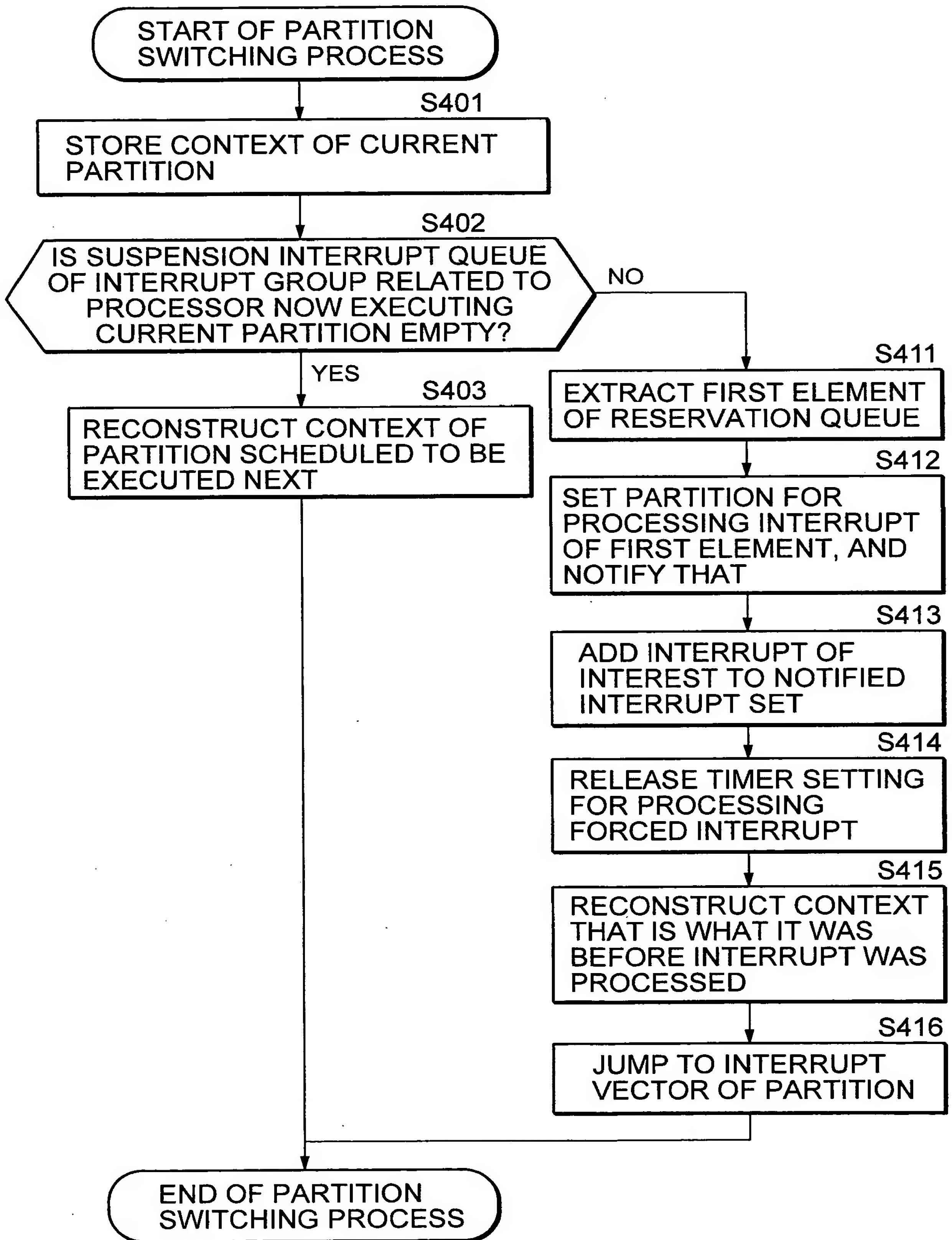




FIG. 18

